

Roll No.

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Total No. of Pages : 02

Total No. of Questions : 07

B.Sc.(IT) (Sem.-3)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : BS-201
Paper ID : [B0409]

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
2. SECTION-B contains SIX questions carrying TEN marks each and a student has to attempt any FOUR questions.

SECTION-A**Q1) Write briefly :**

- a) What is byte addressable memory?
 - b) What is the function of ALU?
 - c) What is vectored and non-vectored interrupt?
 - d) Explain full adder with truth table and diagram.
 - e) Define auto decrement and auto increment mode of addressing.
 - f) Define synchronous and asynchronous communication.
 - g) What are the factors affecting instruction length?
 - h) Define memory access time.
 - i) What do you understand by locality of reference?
 - j) What are Instruction Register and Program Counter used for?
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SECTION-B

- Q2) Explain DMA transfer in a computer system with a block diagram.
- Q3) Explain design of a basic computer and it's working.
- Q4) Explain in detail about memory hierarchy with a diagram.
- Q5) What do you mean by priority interrupt? Explain daisy chaining method of prioritizing multiple interrupts using a diagram.
- Q6) What is virtual memory and how can it be implemented using paging?
- Q7) Explain the various instruction types in detail.

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